



**25th IEEE International Conference on
High Performance Computing, Data, and Analytics**
December 17-20, 2018 | Bengaluru, India | www.hipc.org

Day	Start Time	End Time	Event	Radisson Blu Room
<i>Monday - December 17, 2018 - Day 1</i>				
Mon			<i>Breakfast @ 7:30 am – Lunch @ 1:00 – Dinner @ 7:00 pm</i>	Lobby Area
Mon			<i>Breaks: Morning Break@ 10:30 – Afternoon @ 4:00</i>	
Mon	8:30 AM	6:30 PM	Industry Exhibit Booth Set-up	Central Lobby Area
Mon	8:30 AM	1:00 PM	Workshop 1: PFFT	Arabica
Mon	8:30 AM	4:00 PM	Workshop 2: CFD	Grand Victoria B
Mon	11:00 AM	6:30 PM	EduHiPC Workshop	Robusta
Mon	2:00 PM	6:30 PM	IRUS Session 1: Convergence of HPC with AI	Brain Box
Mon	2:00 PM	6:30 PM	Workshop 3: AIMB	Arabica
Mon	2:00 PM	6:30 PM	Workshop 4: WDSHPC	Grand Victoria A
Mon	2:00 PM	6:30 PM	Student Research Symposium Posters Set-up	Lobby Grand Victoria
Mon	6:30 PM	8:00 PM	Plenary Industry Gala Opening	Grand Victoria
<i>Tuesday – December 18, 2018 – Day 2</i>				
Tue			<i>Breakfast @ 7:00 – Lunch @ 12:00</i>	Lobby Area
Tue			<i>Breaks: Morning @ 9:30 – Afternoon @ 3:00</i>	
Tue	8:00 AM	8:30 AM	HiPC 2018 - Inauguration	Grand Victoria
Tue	8:30 AM	9:30 AM	Keynote Talk 1: Balaraman Ravindran	Grand Victoria
Tue	10:00 AM	12:00 PM	Tech Session 1: Learning	Grand Victoria
Tue	10:00 AM	12:00 PM	Sponsored Tech Session: AMD	Brain Box
Tue	10:00 AM	5:15 PM	Industry Demos & Exhibits	Central Lobby Area
Tue	10:00 AM	5:15 PM	SRS Posters on display	Lobby Grand Victoria
Tue	1:00 PM	3:00 PM	Tech Session 2: Graph Algorithms	Grand Victoria
Tue	1:00 PM	3:00 PM	Sponsored Tech Session: Intel	Brain Box
Tue	3:15 PM	5:15 PM	Tech Session 3: GPUs	Grand Victoria
Tue	3:15 PM	5:15 PM	Sponsored Tech Session: Microsoft	Brain Box
<i>Wednesday – December 19, 2019</i>				
Wed			<i>Breakfast @ 7:00 – Lunch @ 12:00</i>	Lobby Area
Wed			<i>Breaks: Morning Break@ 9:30 – Afternoon @ 3:00</i>	
Wed	8:30 AM	9:30 AM	Keynote Talk 2: Marc Snir	Grand Victoria
Wed	10:00 AM	12:00 PM	Tech Session 4: Linear Algebra and Fault Tolerance	Grand Victoria
Wed	10:00 AM	12:00 PM	IRUS Session 2: Technology for Digital Economy	Brain Box
Wed	10:00 AM	12:00 PM	Sponsored Tech Session: Boston/ARM/Xilinx	Robusta
Wed	10:00 AM	3:00 PM	Industry Demos & Exhibits	Central Lobby Area
Wed	10:00 AM	3:00 PM	SRS Posters on display	Lobby Grand Victoria
Wed	1:00 PM	3:00 PM	Tech Session 5: Algorithms and Data Analysis	Grand Victoria
Wed	1:00 PM	3:00 PM	Sponsored Tech Session: Mellanox	Brain Box
Wed	1:00 PM	3:00 PM	Faculty Summit	Robusta
Wed	3:15 PM	5:15 PM	25th Year Celebration: Special Panel Discussion	Grand Victoria
Wed	5:15 PM	6:30 PM	Social Mixer	Central Lobby Area
Wed	6:30 PM	9:30 PM	Banquet & Awards	Grand Victoria
<i>Thursday – December 20, 2018 – Day 4</i>				
Thu			<i>Breakfast @ 7:30 – Break @ 9:30 – Lunch @ 12:00</i>	Lobby Area
Thu	8:30 AM	9:30 AM	Keynote Talk 3: Srinivasa Devadas	Grand Victoria
Thu	10:00 AM	12:00 PM	Tech Session 6: Applications and System Tools	Grand Victoria
Thu	10:00 AM	12:00 PM	Tutorial 3: HCLib	Arabica
Thu	1:00 PM	3:00 PM	Tutorial 4: Edge oriented IoT	Arabica
Thu	1:00 PM	3:00 PM	Programming Challenge BOF	Grand Victoria

For more detailed information on Keynotes and Technical Sessions, see Advance Program on website, Onsite CPS Mobile App, and 2018 Proceedings of the Conference and Workshops, distributed online to all registrants.
(Changes in schedule or room assignment will be posted in Mobile App and on website)