

2016 HiPC Workshop on Software Composable Infrastructure (SCI)

To be held in conjunction with HiPC 2016 on 19 December 2016, Hyderabad, India

<https://docs.google.com/document/u/1/d/1DpT3Ff4NemPzMdVxNFGdhKolxdcQ7pRUUcSgAMuICAM/pub>

Technical Program

09:00 – 09:15 AM
Opening of SCI Workshop
09:15 – 10:00 AM
Title: Composable Pooled systems with Intel Rackscale Design
Speaker: Mrityika Ganguli
Affiliation: Intel
10:00 – 10:30 AM
Title: Software-Defined Environments for Science
Speaker: Manish Parashar
Affiliation: Rutgers University
10:30 – 11:00 AM
Break
11:00 – 11:30 AM
Technical Paper Presentation
Title: Event Digest for Hyperscale data center telemetry
Authors: Ananth S. Narayan, Lucasz Grzymkowski
Affiliation: Intel
11:30 – 12:15 AM
Title: Applying the ideas of NFV and SDN to Telecom Networks
Speaker: Mythili Vutukuru
Affiliation: Indian Institute of Technology Bombay
12:15 – 12:45 PM
Title: Rack scale architecture Challenges and Opportunities
Speaker: Chakri Padala
Affiliation: Ericsson