



Welcome to the 25th IEEE International Conference on High Performance Computing, Data, and Analytics

December 17-20, 2018 | Bengaluru, India | www.hipc.org

HiPC 2018 CALL FOR PARTICIPATION

HiPC 2018 is the 25th edition of the IEEE International Conference on High Performance Computing, Data, and Analytics. The conference will focus on both HPC as well as Data Science. We will return to our home base in Bangalore (Bengaluru) to mark this anniversary and to hold special celebratory events. We will also use HiPC 2018 to explore programs that expand and enrich the conference offerings, like tutorials and Birds-of-a-Feather meetings, which give added value to conference attendees.

HiPC receives strong industry support from companies operating globally and also established in India. In addition to the two days of industry exhibits, the sponsoring partners to the conference hold industry symposiums to bring together providers and users of HPC in a forum for presenting state-of-the-art in HPC platforms and technologies as well as their applications in Data Science .

HiPC 2018 CALL FOR PAPERS

Starting in 2018, the meeting will be organized under two focus areas: HPC and Data Science. The conference features a robust single track technical program of peer reviewed papers and invited keynote lectures. Complementing the main technical program, HiPC workshops serve to broaden the technical scope of the conference in emerging areas of high performance computing, communication, data and analytics and their applications. The conference encourages student participation by providing an international forum to highlight student research accomplishments in HPC as well as Data Science. The symposium also gives students exposure to the best practices of senior researchers in these areas in academia and industry.

Authors are invited to submit original unpublished research manuscripts that demonstrate current research in all areas of high performance computing, data, and analytics.

For more details see the HiPC website www.hipc.org

GENERAL CO-CHAIRS

Chiranjib Sur, Shell India
Yinglong Xia, Huawei Research, USA

VICE GENERAL CO-CHAIRS

Kishore Kothapalli, IIIT-Hyderabad, India
Anand Panangadan, California State University, Fullerton, USA

PROGRAM CHAIRS

High Performance Computing

Olivier Beaumont, INRIA, France

Data Science

Srinivas Aluru, Georgia Institute of Technology, USA

INDUSTRY LIAISON CO-CHAIRS

Rama Govindaraju, Google, USA
R. Badrinath, Ericsson, India

STEERING COMMITTEE CHAIR

Viktor K. Prasanna, University of Southern California, USA

CO-SPONSORED BY



**Technical Committee on Parallel
Processing**

&

HiPC Education Trust, India

In cooperation with



IMPORTANT DATES

Workshop Proposals Due: **Sunday, March 31, 2018**

Paper Abstract Submission: **Friday, June 8, 2018**

Paper Submission Deadline: **Friday, June 15, 2018**

Author Notifications for Rebuttal: **Friday, August 10, 2018**

Author Rebuttal Response Due: **Friday, August 24, 2018**

Acceptance/Rejection Notification: **Friday, September 7, 2018**

Camera Ready paper Submission Due: **Wednesday, October 3, 2018**

CELEBRATING
25 YEARS