



24th IEEE International Conference on High Performance Computing, Data, and Analytics

December 18-21, 2017 • Le Méridien • Jaipur, India

Day	Start Time	End Time	Event	Le Méridien Room
Mon			<i>Breakfast @ 7:30 – Lunch @ 1:00</i>	Refreshment Lounge
Mon			<i>Breaks: Morning Break@ 10:30 – Afternoon @ 4:00</i>	Lobby Area
Mon	8:30 AM	6:30 PM	Industry Exhibit Booth Set-up	Diwan E Aam
Mon	8:30 AM	6:30 PM	Workshop 1: BIGDF	Diwan E Khas
Mon	8:30 AM	6:30 PM	Workshop 2: CFD	Aravali Hall
Mon	11:00 AM	1:00 PM	Academic BOF1: GEM5	Surya Vilas
Mon	2:00 PM	4:00 PM	Tutorial 1: Power Optimization	Surya Vilas
Mon	4:30 PM	6:30 PM	Tutorial 2: FPGAs for HPC	Surya Vilas
Mon	4:30 PM	6:30 PM	Student Research Symposium Poster Set-up	Royal Lounge
Mon	6:30 PM	8:00 PM	Plenary Industry Gala Opening	TBA
Tue			<i>Breakfast @ 7:00 – Lunch @ 12:00</i>	Refreshment Lounge
Tue			<i>Breaks: Morning @ 9:30 – Afternoon @ 3:00 – Late @ 5:15 PM</i>	Lobby Area
Tue	8:00 AM	8:30 AM	Plenary HiPC 2017 Inauguration	Amer Ballroom
Tue	8:30 AM	9:30 AM	Keynote Talk 1: Parthasarathy Ranganathan (Google)	Amer Ballroom
Tue	10:00 AM	12:00 PM	Tech Session 1: Graph Algorithms	Amer Ballroom
Tue	10:00 AM	12:00 PM	IRUS 1: Applications of Machine Learning in e-commerce	Aravali Hall
Tue	10:00 AM	12:00 PM	Sponsored Tech Session: SanDisk	Diwan E Khas
Tue	10:00 AM	5:30 PM	Industry Demos & Exhibits	Diwan E Aam
Tue	10:00 AM	5:30 PM	SRS Posters on display	Royal Lounge
Tue	1:00 PM	3:00 PM	Tech Session 2: Architecture and Communication	Amer Ballroom
Tue	1:00 PM	3:00 PM	Sponsored Tech Session: Microsoft	Diwan E Khas
Tue	3:15 PM	5:15 PM	Tech Session 3: Algorithms	Amer Ballroom
Tue	3:15 PM	5:15 PM	IRUS 2: Performance Challenge in Deep Learning and Scientific Computing	Aravali Hall
Tue	5:30 PM	7:30 PM	Plenary Panel: HPC in India	Amer Ballroom
Wed			<i>Breakfast @ 7:30 – Lunch @ 12:00</i>	Refreshment Lounge
Wed			<i>Breaks: Morning Break@ 9:30 – Afternoon @ 3:00</i>	Lobby Area
Wed	8:30 AM	9:30 AM	Keynote Talk 2: Rajeev Rastogi (Amazon)	Amer Ballroom
Wed	10:00 AM	12:00 PM	Tech Session 4: Big Data, Machine Learning and Optimization	Amer Ballroom
Wed	10:00 AM	12:00 PM	Faculty Summit	Surya Vilas
Wed	10:00 AM	5:30 PM	Industry Demos & Exhibits	Diwan E Aam
Wed	10:00 AM	5:30 PM	SRS Posters on display	Royal Lounge
Wed	1:00 PM	3:00 PM	Tech Session 5: Graph Algorithms and GPU	Amer Ballroom
Wed	1:00 PM	3:00 PM	Sponsored Tech Session: Xilinx	Diwan E Khas
Wed	3:30 PM	5:30 PM	Plenary Panel: Machine Learning and Mathematical Modeling	Diwan E Khas
Wed	6:30 PM	9:30 PM	Banquet & Awards	Amer Ballroom
Thu			<i>Breakfast @ 7:30 – Lunch @ 12:00</i>	Refreshment Lounge
Thu			<i>Morning Break@ 9:30</i>	Lobby Area
Thu	8:30 AM	9:30 AM	Keynote Talk 3: Ian Foster (Argonne Nat Lab & U of Chicago)	Amer Ballroom
Thu	10:00 AM	12:00 PM	Tech Session 6: System Software	Amer Ballroom
Thu	10:00 AM	12:00 PM	Academic BOF: Parallel LSG Computation	Aravali Hall
Thu	1:00 PM	3:00 PM	Tech Session 7: GPU Frameworks and Applications	Amer Ballroom

For more detailed information on Keynotes and Technical Sessions, see Advance Program on website, Onsite CPS Mobile App, and 2017 Proceedings of the Conference and Workshops, distributed online to all registrants. (Changes in schedule or room assignment will be posted in Mobile App and on website)