



हाइपीसी HiPC

24th IEEE International Conference on

High Performance Computing, Data, and Analytics

December 18-21, 2017 | Le Méridien Hotel | Jaipur, India

www.hipc.org

HiPC 2017 PROGRAM & EVENTS

HiPC 2017 will be the 24th edition of this conference. It serves as a forum to present current work by researchers from around the world as well as highlight activities in Asia in the area of high performance computing. The meeting focuses on all aspects of HPC systems and their scientific, engineering, and commercial applications. In 2016, to keep pace with new computing trends, the conference added two new areas of interest to its name, Data and Analytics, as reflected in the 2017 Call for Papers.

CONFERENCE OVERVIEW: The first day will feature four workshops followed by the main technical program on days 2, 3 and 4 showcasing three keynote speakers and three days of single track presentations of peer reviewed papers from all over the world. In parallel on days 2 and 3, there will be a full program of industry exhibits and events.

INDUSTRY PARTICIPATION: The HiPC conference has a history of active industry participation in its steering, organizing and program committees. The conference encourages industry participation in the main technical program, workshops, the Industry, Research and User Symposium (IRUS) and vendor exhibition. Various sponsorship opportunities are available as described on the conference website. Titanium/Platinum sponsors are also provided a forum to drive Industry BOF sessions.

STUDENT PROGRAMS: HiPC 2017 will hold the 10th annual Student Research Symposium (SRS) aimed at fostering student research, and providing a forum to present student research in HPC and related areas in a poster session. The conference also plans to host industry sponsored Student Parallel Programming Challenges to highlight the value of code parallelism, parallel algorithms, and the role of mathematics in the fastest working code. See website for details.

HiPC 2017 WORKSHOPS

See website for each workshop's Call for Papers (deadlines below)

- 1 - Workshop on Foundations of Big Data Computing (BigDF)
- 2 - Third Workshop on Computational Fluid Dynamics (CFD)
- 3 - Workshop on Dynamic Data Driven Smart Systems (DDDSS)
- 4 - Workshop on Software Composable Infrastructure (SCI)

IMPORTANT DATES

Author Rebuttal Response Due: **Tuesday, August 15, 2017**
Acceptance/Rejection Notification: **Friday, Sept 8, 2017**
Camera Ready paper Submission Due: **Tuesday, October 3, 2017**

BigDF workshop paper submission Due: **Friday, August 25, 2017**
CFD workshop paper submission Due: **Friday, August 25, 2017**
DDDSS workshop paper submission Due: **Friday, Sept 15, 2017**
SCI workshop paper submission Due: **Thursday, August 31, 2017**

Student Research Symposium submissions Due: **Saturday, Sept 16, 2017**

GENERAL CO-CHAIRS

Chiranjib Sur, Shell India
Yinglong Xia, Huawei Research, USA

VICE GENERAL CO-CHAIRS

Kishore Kothapalli, IIIT-Hyderabad, India
Anand Panangadan, California State University, Fullerton, USA

PROGRAM CHAIR

Ümit V. Çatalyürek, Georgia Institute of Technology, USA

PROGRAM VICE-CHAIRS

Algorithms: Olivier Beaumont, INRIA, France
Applications: Ananth Kalyanaraman Washington State University, USA
Architecture: Yuan Xie, University of California at Santa Barbara, USA
System Software: Gagan Agrawal, The Ohio State University, USA

INDUSTRY LIAISON CO-CHAIRS

Rama Govindaraju, Google, USA
Jigar Halani, Wipro, India
Vivek Yadav, FullStackNet, India

WORKSHOPS CO-CHAIRS

Manish Parashar, Rutgers University, USA
Saumil Merchant, Shell, India

STUDENT RESEARCH SYMPOSIUM CO-CHAIRS

Kishore Kothapalli, IIIT-Hyderabad, India
Madhura Purnaprajna, Amrita University, India
Ashok Srinivasan, Florida State University, USA

INDUSTRY, RESEARCH & USERS SYMPOSIUM (IRUS) CO-CHAIRS

R. Badrinath, Ericsson, India
Seetha Rama Krishna Nookala, Intel, India

LOCAL ARRANGEMENTS CHAIR

Ajay K Gupta, Western Michigan University, USA

PROCEEDINGS CHAIR

Ren Chen, University of Southern California, USA

PRODUCTION CHAIR

Sally Jelinek Westrom, EDA Inc. USA

STEERING COMMITTEE CHAIR

Viktor K. Prasanna, University of Southern California, USA

CO-SPONSORED BY

IEEE  computer society

Technical Committee on Parallel
Processing & HiPC Education Trust, India

In cooperation with

