HiPC 2017 CALL FOR PAPERS

Authors are invited to submit original unpublished research manuscripts that demonstrate current research in all areas of high performance computing, data, and analytics.

For more details see the HiPC website at **www.hipc.org**

TOPICS INCLUDE (BUT ARE NOT LIMITED TO):

- Algorithmic Techniques to Improve Energy and Power
- Big Data Analytics Systems and Software Architectures
- Cache/Memory Architecture for High Performance Computing
- Compiler Technologies for High Performance Computing
- Concurrent Algorithms and Data Structures
- Cross-Cutting Methods such as Co-Design of Parallel Algorithms, Software, and Architectures
- Dealing with Uncertainties, Resilient/Fault-Tolerant Algorithms and Systems
- Design and Application of Parallel and Distributed Algorithms and Systems
- Emerging Applications such as Biotechnology and Nanotechnology
- Exascale Computing, Cloud Platforms, Data Center Architectures and Services
- Hardware Acceleration for Parallel Applications
- High Performance/Scalable Storage Systems
- Hybrid Parallel Programming with GPUs and Accelerators
- Interconnection Networks and Architectures
- Large Scale Recommendation Systems, Graph Analytics and Computing Challenges
- Load Balancing, Scheduling and Resource Management
- Operating Systems for Scalable High Performance Computing
- Parallel Languages, Programming Environments, and Performance Assessment
- Power-Efficient and Reconfigurable Architectures
- Quantum and Bio-Inspired Algorithms and Architectures
- Scientific/Engineering/Industrial Applications and Workloads
- Software Support and Advanced Micro-architecture Techniques
- Streaming Algorithms
- Parallelism in Visualization and Visual Analytics

GENERAL CO-CHAIRS
Chiranjib Sur, Shell India
Yinglong Xia, Huawei Research, USA

VICE GENERAL CO-CHAIRS
Kishore Kothapalli, IIIT-Hyderabad, India
Anand Panangadan, California State University, Fullerton, USA

PROGRAM CHAIR
Ümit V. Çatalyürek, Georgia Institute of Technology, USA

PROGRAM VICE-CHAIRS

Algorithms: Olivier Beaumont, INRIA, France
Applications: Ananth Kalyanaraman Washington State University, USA
Architecture: Yuan Xie, University of California at Santa Barbara, USA
System Software: Gagan Agrawal, The Ohio State University, USA

INDUSTRY LIAISON CO-CHAIRS
Rama Govindaraju, Google, USA
Jigar Halani, Wipro, India

WORKSHOPS CO-CHAIRS
Manish Parashar, Rutgers University, USA
Saumil Merchant, Shell, India

STUDENT RESEARCH SYMPOSIUM CO-CHAIRS
Kishore Kothapalli, IIIT-Hyderabad, India
Ashok Srinivasan, Florida State University, USA

INDUSTRY, RESEARCH & USER SYMPOSIUM CO-CHAIRS
R. Badrinath, Ericcson, India
Seetha Rama Krishna Nookala, Intel, India

LOCAL ARRANGEMENT CHAIR
Ajay K Gupta, Western Michigan University, USA

STEERING COMMITTEE CHAIR
Viktor K. Prasanna, University of Southern California, USA

IMPORTANT DATES

Workshop Proposals Due: Friday, March 31, 2017
Paper Abstract Submission: Monday, June 5, 2017
Paper Submission Deadline: Thursday, June 15, 2017
Author Notifications for Rebuttal: Thursday, August 10, 2017
Author Rebuttal Response Due: Tuesday, August 15, 2017
Acceptance/Rejection Notification: Friday, September 8, 2017
Camera Ready paper Submission Due: Tuesday, October 3, 2017

CO-SPONSORED BY

IEEE ⊕ computer society

Technical Committee on Parallel Processing & HiPC Education Trust, India

In cooperation with   acm