

# 16TH INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE COMPUTING

December 16-19, 2009 | Kochi (Cochin), INDIA  
<http://www.hipc.org>



# हाइपीसी HiPC



## PROGRAM HIGHLIGHTS

The 16th International Conference on High Performance Computing (HiPC 2009), to be held in Kochi (Cochin), India, during December 16-19, 2009, will serve as a forum for presenting current work by researchers from around the world as well as to highlight activities in Asia, in the area of high performance computing.

### KEYNOTE SPEAKERS

- > Bill Dally, NVIDIA and Stanford University, USA  
[The End of Denial Architecture and the Rise of Throughput Computing](#)
- > Justin R. Rattner, Intel Corp., USA  
[Bringing Supercomputing to the Masses](#)
- > Dan Reed, Microsoft, USA  
[Computing at the Crossroads](#)

### CONTRIBUTED PAPERS

The technical program will consist of 49 contributed papers chosen from 261 submissions from 20 countries. The papers will be presented in 8 sessions.

### PANEL ON CYBERINFRASTRUCTURE

On Friday, December 18th, the conference will feature a plenary panel focussed on Scientific Frontiers, High Performance Computing and Cyberinfrastructure, which will be moderated by Thomas Zacharia from Oak Ridge National Laboratory, USA, and will include distinguished panelists including Edward Seidel (National Science Foundation, USA), Horst Simon (Lawrence Berkeley National Laboratory, USA), and Rick Stevens (Argonne National Laboratory, USA).

### STUDENT RESEARCH SYMPOSIUM

HiPC 2009 will feature the second student research symposium on High Performance Computing on Wednesday, December 16th, aimed at stimulating and fostering student research, and providing an international forum to highlight student research accomplishments. The one-day symposium will feature brief presentations by student authors on their research and a poster exhibit, and will be followed by a reception and industry-student mixer.

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## WORKSHOPS

HiPC 2009 will host 4 workshops on Wednesday, December 16, 2009

- > Workshop on Service Oriented Computing
- > Workshop on Ubiquitous Reconfigurable Computing Platforms for Adaptive, Secure and Distributed Computing Systems
- > Workshop on Grid and Utility Computing
- > Workshop on Thermal-Power Challenges and Trends in Computing Systems

## TUTORIAL

HiPC 2009 will feature the following tutorial on Wednesday, December 16, which will be open to all conference attendees.

- > Modeling Complex Storage Systems  
Dr. Thirumale Niranjan and Dr. Sai Susarla, NetApp Advanced Technology Group, Bangalore, India  
Dr. Chiranjib Bhattacharyya, Associate Professor, Indian Institute of Science, Bangalore, India

## USER/INDUSTRY SYMPOSIUM

The symposium will bring together providers and users of HPC to provide a forum for presenting the state-of-the-art in HPC platforms and technologies, for discussing best practices and for exchanging experiences. This two-day symposium, to be held on Thursday, December 17th and Friday, December 18th, will feature a panel session on HPC future directions and challenges, and sessions on a range of topics including migration to cloud computing, performance tuning toolkits, and system design for data center scale.

## INDUSTRY/RESEARCH EXHIBITS

This two-day exhibition session, to be held on Thursday, December 17th and Friday, December 18th, will consist of exhibits / presentations from vendor companies and R&D laboratories.

## CONFERENCE REGISTRATION

The conference registration includes attendance to all workshops and technical sessions and a copy of the HiPC proceedings. Visit the conference website for further details.

## INFORMATION FOR INDIA-BASED PARTICIPANTS

Registration at reduced rates is made possible for India-based faculty, students, and researchers through sponsorships from India based industries and organizations. Travel scholarships for India-based students are available on a merit basis to promote HPC research in India.

## SPONSORSHIP OPPORTUNITIES

The HiPC conference is annually sponsored by several Indian and international organizations, institutions, and companies. For sponsorship opportunities, contact the Industry Co-Chairs at [industry@hipc.org](mailto:industry@hipc.org).

For further details of the meeting, visit <http://www.hipc.org>.

## CONFERENCE ORGANIZATION

### GENERAL CO-CHAIRS

MANISH PARASHAR, Rutgers University, USA  
RAJEEV MURALIDHAR, Intel, India

### STEERING COMMITTEE CHAIR

VIKTOR K. PRASANNA, Univ. of Southern California, USA

### PROGRAM CHAIR

YUANYUAN YANG,  
State University of New York at Stony Brook, USA

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APPLICATIONS:  
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ARCHITECTURE:  
ANGELOS BILAS, FORTH and University of Crete, Greece

COMMUNICATION NETWORKS:  
XIN YUAN, Florida State University

MOBILE AND SENSOR COMPUTING:  
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GAGAN AGRAWAL, Ohio State University

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