

HPC Users Symposium

at 14th IEEE International Conference on High Performance Computing (HiPC)

<http://www.hipc.org>
Goa, India

December 20, 2007

SYMPOSIUM PROGRAM HIGHLIGHTS

Mini Symposium on High Performance Computing Technologies, Applications and Experience will be held in conjunction with the 14th IEEE International Conference on High Performance Computing (HiPC 2007).

The mini symposium will bring together providers and users of HPC to provide a forum for presenting the state-of-the-art in HPC platforms and technologies, for discussing best practices and for exchanging experiences. HPC applications and the experiences of application developers will be the focus of this mini symposium. For further details, contact the Symposium Co-Chair TK Ramesh at tirumale.k.ramesh@boeing.com

Keynote at the HPC Users Symposium

“How High Performance Computing is Transforming Scientific Research”

Dr. Ralph Roskies
Scientific Director, Pittsburgh Supercomputing Center, USA

Abstract

Not only does HPC change the kinds of problems we can attack; it also changes the way in which we carry out scientific research. Drawing on examples from the Pittsburgh Supercomputing Center and the TeraGrid, the speaker will illustrate the impact that high performance computing is having on the conduct of Science.

Symposium Technical Program

The symposium technical program will consist of a keynote presentation, 3 invited papers and 5 contributed papers with representative contributions from industry, academia and government organizations addressing the use and applications of high performance computing and its impact on current HPC technologies. Please see for details on the next page.



Symposium ORGANIZATION

SYMPOSIUM CO-CHAIRS*

TK Ramesh, The Boeing Company, USA
Santosh Srinivasan, Talentain Technologies, India
Venkat Ramana, Hinditron-CRAY, India

SYMPOSIUM COMMITTEE

* Includes
Ramamurthy Badrinath, HP, India
Haresh Bhatt, SAC-ISRO, India

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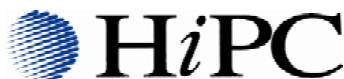
Manish Parishar, Rutgers Univ., USA
Ramamurthy Badrinath, HP, India

STEERING COMM. CHAIR

Viktor K. Prasanna, Univ. of Southern California, USA



Sponsored by: IEEE Computer Society Technical Committee on Parallel Processing, ACM SIGARCH, European Association for Theoretical Computer Science, IFIP Working Group on Concurrent Systems, National Association of Software and Service Companies, and Manufacturers' Association for Information Technology.



[HPC Users Symposium Program Highlights - continued overleaf]