

## HiPC 2007 Schedule

	Tuesday, 18 Dec	Wednesday, 19 Dec	Thursday, 20 Dec	Friday, 21 Dec
7:00 - 7:30 am		Breakfast (Larange Restaurant)		
7:30 - 8:00 am	Breakfast (Larange Restaurant)	Opening Remarks Eka Intro. (A. Nanda, CRL) (Grande Sala)	Breakfast (Larange Restaurant)	Breakfast (Larange Restaurant)
8:00 - 8:30 am				
8:30 - 9:00 am				
9:00 - 9:30 am	Tutorial 1	Workshop 2	Workshop 3	Keynote Address IV Vipin Kumar (Grande Sala)
9:30 - 10:00 am		Break	Break	Break
10:00 - 10:30 am		Parallel Technical Session I (6 papers) (Grande Sala)	Parallel Technical Session IV (5 papers) (Sala de Banquet)	Parallel Technical Session VII (5 papers) (Grande Sala)
10:30 - 11:00 am	Break	Parallel Technical Session II (6 papers) (Sala de Banquet)	User Symposium Part I (5 talks) (HRL)	Parallel Technical Session VIII (5 papers) (Sala de Banquet)
11:00 - 11:30 am	Tutorial 1			
11:30 am - 12:00	Workshop 2			
12:00 - 12:30 pm	Workshop 3		Lunch (Larange Restaurant)	Lunch (Larange Restaurant)
12:30 - 1:00 pm		Lunch (Larange Restaurant)		
1:00 - 1:30 pm	Lunch (Larange Restaurant)	Keynote Address II David Keyes (Grande Sala)	Parallel Technical Session V (5 papers) (Grande Sala)	Keynote Address V Yale Patt (Grande Sala)
1:30 - 2:00 pm		Break (Poster setup Time)	Parallel Technical Session VI (5 papers) (Sala de Banquet)	Break
2:00 - 2:30 pm	Tutorial 2		User Symposium Part II (5 talks) (HRL)	
2:30 - 3:00 pm	Workshop 1		Break	Parallel Technical Session IX (5 papers) (Grande Sala)
3:00 - 3:30 pm	Workshop 4			Parallel Technical Session X (5 papers) (Sala de Banquet)
3:30 - 4:00 pm		Poster Presentations (2-3 min. per poster)		
4:00 - 4:30 pm	Break		Industry Session (Grande Sala) 3:15 pm - 6:15pm	
4:30 - 5:00 pm	Tutorial 2	Poster Session  (Multi function area)		
5:00 - 5:30 pm	Workshop 1			
5:30 - 6:00 pm	Workshop 4	Plenary Session Best Paper and Best		
6:00 - 6:30 pm		Invited Plenary Talk "Eka" Supercomputer A. K. Nanda, CRL (Grand Sala)		
6:30 - 7:00 pm				
7:00 - 7:30 pm	Tutorial 1 (half-day) – Sensor Networks Tutorial 2 (half-day) - Programming Models and Compiler Optimizations for GPUs and Multi-core Processors		Conference Banquet, Cultural Program, and Dinner  (Sponsored by Dell and Platform Computing)	
7:30 - 8:00 pm				
8:00 - 8:30 pm	Workshop 1 - Service-Oriented Engineering and Optimization (half-day) Workshop 2 - New Horizons in Compilers (half-day) Workshop 3 - Grid and Utility Computing (half-day) Workshop 4 - Storage Technologies in Computing Clusters & Datacenter Environments (half-day)			
8:30 - 9:00 pm				
9:00 - 9:30 pm				

- Registration desk will be open from 8:00 am to 4:00 pm from Tuesday to Friday (December 18 - 21).
- Conference registration fee includes breakfast, lunch, and refreshments from December 18 - 21, and banquet on December 20.
- Conference site: Cidade-de-Goa, Goa, India.