

# HPC Users Symposium at HiPC 2007 on HPC Technologies, Applications and Experiences

Goa, India  
December 20, 2007



Held in conjunction with the  
14<sup>th</sup> IEEE International Conference on  
High Performance Computing  
(HiPC 2007)  
Goa, India  
December 18-21, 2007

## SYMPOSIUM IMPORTANT DATES

August 17, 2007	Submissions
September 14, 2007	Accept/Reject Decisions
December 20, 2007	Symposium

## SYMPOSIUM CO-CHAIRS

T. K. Ramesh, The Boeing Company, USA  
R. Tupuri, AMD, India

## HiPC 2007 GENERAL CO-CHAIRS

M. Parashar, Rutgers Univ., USA  
R. Badrinath, HP, India

## HiPC STEERING COMMITTEE CHAIR

V. Prasanna, Univ. of Southern California, USA

<http://www.hipc.org>

## CALL FOR EXTENDED ABSTRACTS

The primary goal of this symposium is to bring together providers and users of High Performance Computing (HPC). The event will provide a forum for presenting the state-of-the-art in HPC platforms and technologies, for discussing best practices, and for exchanging experiences and lessons learned.

The one-day symposium will feature keynotes by HPC users as well as contributed talks by leading experts from industry, research laboratories and universities, and a short user's panel and open discussions at the end. The symposium will be held in conjunction with HiPC 2007. The conference emphasizes the design and analysis of high performance parallel, distributed, networked and mobile sensor-based computing systems, and their scientific, engineering, and commercial applications. HiPC 2007, the 14th conference in this series, will be held in Goa, India during December 18-21, 2007.

An industrial exhibition will run in parallel with HPC Users Symposium, which will provide an opportunity for showcasing HPC vendor technologies. Please visit the conference website <http://www.hipc.org> for more details.

**Call for Extended Abstracts:** HPC users, practitioners and domain application developers are invited to submit extended abstracts (2-3 pages in length) describing HPC platforms usage, enabling technologies, applications and experiences. Note that the presentation should briefly introduce the application domain and primarily focus on how HPC resources and technologies are used to address application requirements. Extended abstracts shall be submitted by August 17, 2007 via email to T. K. Ramesh, HPC Users Symposium co-chair, at [tirumale.k.ramesh@boeing.com](mailto:tirumale.k.ramesh@boeing.com).



Sponsored by: IEEE Computer Society Technical Committee on Parallel Processing (TCPP), ACM Special Interest Group on Computer Architecture (SIGARCH), European Association for Theoretical Computer Science, IFIP Working Group on Concurrent Systems, National Association of Software and Service Companies (NASSCOM), and Manufacturers' Association for Information Technology (MAIT).

