

```

*****
*
*          =====>   CALL FOR PAPERS   <=====
*
*                   HiPC'2001
*
*       8th International Conference on High Performance Computing
*
*                   http://www.hipc.org
*
*       Submission Deadline: May 4, 2001
*
*****

```

HiPC 2001  
 December 17-20, 2001  
 Hyderabad, India

The 8th annual International Conference on High Performance Computing (HiPC 2001) will be held in Hyderabad, an emerging center for Information Technology in India, during December 17-20, 2001. It will serve as a forum to present current work by researchers from around the world as well as highlight activities in Asia in the high performance computing area. The conference has a history of attracting participation from reputed researchers worldwide. The seventh meeting held in Bangalore, India, in December 2000 had 46 contributed papers that were selected from submissions from 12 countries. The conference had tutorials on cutting edge topics in high performance computing and related areas by leading researchers from academia and industry. Please refer to <http://www.hipc.org/> for more information on previous meetings.

Hyderabad, known as the City of Pearls, is the fifth largest metropolitan city in India and is fast emerging as the hub of Information Technology. Many multi-national companies including Microsoft, Motorola, Oracle, GE Capital, IBM, among others have set up development units in Hyderabad. It is home to upcoming premier universities such as Indian Institute of Information Technology and Indian School of Business and also some of the oldest and prestigious universities in India including Osmania University and Jawaharlal Nehru Technical University. Hyderabad is highly cosmopolitan and presents a mix of various cultures which is reflected in its ethnic, cultural, architectural and culinary diversity.

HiPC 2001 will emphasize the design and analysis of high performance computing and networking systems and their scientific, engineering, and commercial applications. In addition to technical sessions of contributed paper presentations, the conference will offer invited presentations, a poster/presentation session, tutorials, vendor presentations, and exhibits.

**COSPONSORED BY**

Indian Institute of Information Technology, Hyderabad  
 IEEE Computer Society Technical Committee on Parallel Processing  
 ACM SIGARCH  
 European Association for Theoretical Computer Science

**HELD IN COOPERATION WITH**

Indian Institutes of Technology  
Software Technology Parks of India  
Tata Institute of Fundamental Research, India  
Centre for Artificial Intelligence Research(CAIR), India  
Centre for Development of Advanced Computing(C-DAC), India  
CSIR Centre for Mathematical Modeling and Computer Simulation, India

#### MEETING INFORMATION

The advance program will be available in July 2001. Check [www.hipc.org](http://www.hipc.org) for updated information.

#### IMPORTANT DATES

May	04, 2001	Conference Paper Due
May	15, 2001	Tutorial Proposal Due
July	02, 2001	Notification of Acceptance/Rejection
August	15, 2001	Camera-Ready Paper Due
October	02, 2001	Poster/Presentation Summary Due

-----

=====  
CALL FOR PARTICIPATION  
=====

#### CALL FOR PAPERS

Authors are invited to submit original unpublished manuscripts that demonstrate current research in areas of high performance computing including design and analysis of parallel and distributed systems, embedded systems, and their applications in scientific, engineering, and commercial areas. Topics of interest include but are not limited to:

- |                                      |                                       |
|--------------------------------------|---------------------------------------|
| * Parallel Algorithms                | * Scientific Computation              |
| * Parallel Architectures             | * Visualization                       |
| * Parallel Languages & Compilers     | * Network and Cluster-Based Computing |
| * Distributed Systems                | * Signal & Image Processing Systems   |
| * Programming Environments           | * Supercomputing Applications         |
| * Memory Systems                     | * Internet and WWW-based Computing    |
| * Multimedia and High Speed Networks | * Scalable Servers                    |
| * Wireless Mobile Computing          | * Embedded Systems                    |

To submit an original research paper, send your complete manuscript (not to exceed 15 double-spaced pages of text using 12 point size type on 8.5 x 11 inch pages) to the Program Chair. References, figures, tables, etc. may be included in addition to the fifteen pages of text.

Manuscript submission procedures are available over the Web at <http://www.upb.de/hipc2001>. For those who have only e-mail access, send an e-mail message to [hipc2001@upb.de](mailto:hipc2001@upb.de). Electronic submissions must be in the form of a readable postscript file containing the following header information in ASCII form: title, author name(s), abstract, postal address, e-mail address, and telephone and fax numbers. The header (in ASCII) should be followed by the PostScript version of the complete manuscript (including title, author names, affiliation, and abstract). Authors should submit a

correct PostScript (level 2) file of their paper to be considered and make sure that the PostScript file will print on a PostScript printer that uses 8.5 x 11 inch size (letter size) paper.

Hard copy submissions are permitted and, like electronic submissions, must be received by May 4, 2001. Please send six copies of the manuscript to the Program Chair.

Fax submissions will not be considered.

Manuscripts must be received by May 4, 2001. All manuscripts will be reviewed. Submissions received after the due date or exceeding the length limit may not be considered. Notification of review decisions will be mailed by July 2, 2001. Camera-ready papers are due August 15, 2001. Proceedings will be available at the conference and also on the Web after the conference.

#### PROGRAM CHAIR

Burkhard Monien  
Department of Math. & Comp. Science  
Universit t Paderborn  
F rstenallee 11  
D-33102 Paderborn, Germany  
Internet: bm@uni-paderborn.de

#### PROGRAM VICE CHAIRS

##### Algorithms

Bruce M. Maggs, Akamai Technologies and Carnegie Mellon University, USA

##### Applications

Horst D. Simon, NERSC, Lawrence Berkeley National Laboratory, USA

##### Architecture

Allan Gottlieb, New York University and NEC Research, USA

##### Communication Networks

Cauligi S. Raghavendra, University of Southern California, USA

##### Systems Software

Guang R. Gao, University of Delaware, USA

#### PROGRAM COMMITTEE

##### Algorithms

Micah Adler, University of Massachusetts, USA  
Richard Anderson, University of Washington, USA  
John Byers, Boston University, USA  
Ramesh Hariharan, Indian Institute of Science, Bangalore, India  
Michael Mitzenmacher, Harvard University, USA  
S. Muthukrishnan, AT&T Research, USA  
Stephane Perennes, INRIA, France  
Sushil K. Prasad, Georgia State University, USA  
Rajmohan Rajaraman, Northeastern University, USA  
Andrea Richa, Arizona State University, USA  
Christian Scheideler, Johns Hopkins University, USA

Assaf Schuster, Technion, Israel Institute of Technology, Israel

#### Applications

Patrick Amestoy, ENSEEIHT-IRIT, Toulouse, France  
Rupak Biswas, NASA Ames Research Center, USA  
Alvaro Coutinho, COPPE, Rio de Janeiro, Brazil  
Chris Ding, NERSC, Lawrence Berkeley National Laboratory, USA  
Jack Dongarra, Innovative Computing Lab, University of Tennessee, USA  
Robert Harrison, Pacific Northwest National Laboratory, USA  
Steve Jardin, Princeton Plasma Physics Laboratory, USA  
Ken Miura, Fujitsu, Japan  
Franz-Josef Pfreundt, ITWM, Universit t Kaiserslautern, Germany  
Thomas Zacharia, Oak Ridge National Laboratory, USA

#### Architecture

Alan Berenbaum, Agere Systems, USA  
Doug Burger, University of Texas at Austin, USA  
Vijay Karamcheti, New York University, USA  
Gyungho Lee, Iowa State University, USA  
Daniel Litaize, Institut de Recherche en Informatique de Toulouse, France  
Vijaykrishnan Narayanan, Pennsylvania State University, USA  
Yale Patt, University of Texas at Austin, USA  
Shuichi Sakai, University of Tokyo, Japan  
Jaswinder Pal Singh, Princeton University, USA  
Mateo Valero, Universidad Politecnica de Catalunya, Spain  
Sudhakar Yalamanchili, Proceler Inc, USA

#### Communication Networks

Ian F. Akyildiz, Georgia Institute of Technology, USA  
Hari Balakrishnan, MIT, USA  
Joseph Bannister, University of Southern California/ISI, USA  
Deborah Estrin, University of California, Los Angeles/ISI, USA  
Ramesh Govindan, University of Southern California/ISI, USA  
Anurag Kumar, Indian Institute of Science, Bangalore, India  
T. V. Lakshman, Lucent Technologies, USA  
Masayuki Murata, Osaka University, Japan.  
Marco Ajmone Marsan, Politecnico de Torino, Italy  
Douglas Maughan, DARPA ITO, USA  
K. G. Shin, University of Michigan, USA

#### Systems Software

Pradeep Dubey, IBM India Research Lab, India  
R. Govindarajan, Indian Institute of Science, Bangalore, India  
Wen-mei Hwu, University of Illinois at Urbana Champaign, USA  
Laxmikant Kale, University of Illinois at Urbana Champaign, USA  
Bernard Lecussan, SupAero, Toulouse, France  
Yoichi Muraoka, Waseda University, Tokyo, Japan  
Keshav Pingali, Cornell University, USA  
Martin Rinard, MIT Lab for Computer Science, USA  
Vivek Sarkar, IBM T.J. Watson Research Center, USA  
V.C. Sreedhar, IBM T.J. Watson Research Center, USA  
Theo Ungerer, Universit t Karlsruhe, Germany

#### BEST PAPER AWARDS

These awards, sponsored by Infosys, will be awarded to outstanding contributed papers in the Systems area and in the Algorithms and Applications area.

AWARDS CHAIR

Arvind, MIT

POSTER/PRESENTATION SESSION

In addition to parallel sessions of contributed papers, a plenary poster/presentation session emphasizing novel applications of high performance computing is planned. This session will be held concurrently with a reception followed by a dinner. It will offer brief presentation time for each poster and will be followed by a walk-up and talk setting. To be considered, send a 5 page summary of your work to the Poster/Presentation Chair by October 2, 2001. For additional details contact the Poster/Presentation Chair.

POSTER/PRESENTATION CHAIR

Sartaj Sahni, University of Florida, Internet: sahni@cise.ufl.edu

TUTORIALS

Proposals are solicited for tutorials to be held at the meeting. Interested individuals should submit a proposal by May 15, 2001 to the Tutorials Chair. The proposal should include a brief description of the intended audience, a lecture outline, and vita for lecturer(s).

TUTORIALS CHAIR

Manavendra Misra, KBkids.com, Internet: manav@kbids.com

EXHIBITS/VENDOR PRESENTATIONS

Companies and R&D laboratories are encouraged to present their exhibits at the meeting. In addition, a full day of vendor presentations is planned. For details, companies are encouraged to contact the Exhibits Chair by August 31, 2001.

EXHIBITS CO-CHAIRS

Sudheendra Hangal, Sun Microsystems, Internet: hangal@eng.sun.com

Shankar Velayudhan, Mindteck (India) Limited, Internet: shankarv@mindteck.com

-----

=====  
HiPC 2001 ORGANIZATION  
=====

GENERAL CO-CHAIRS

Viktor K. Prasanna, University of Southern California  
Internet: prasanna@ganges.usc.edu

Sriram Vajapeyam, Indian Institute of Science, Bangalore  
Internet: sriram@csa.iisc.ernet.in

VICE GENERAL CHAIR

Sajal K. Das, The University of Texas at Arlington  
Internet: das@cse.uta.edu

PROGRAM CHAIR

Burkhard Monien, Universit,t Paderborn, Germany  
Internet: bm@uni-paderborn.de

STEERING CHAIR

Viktor K. Prasanna, University of Southern California

AWARDS CHAIR

Arvind, MIT

POSTER/PRESENTATION CHAIR

Sartaj Sahni, University of Florida

TUTORIALS CHAIR

Manavendra Misra, KBkids.com

KEYNOTE CHAIR

Vipin Kumar, University of Minnesota

EXHIBITS CO-CHAIRS

Sudheendra Hangal, Sun Microsystems

Shankar Velayudhan, Mindteck (India) Limited

SCHOLARSHIPS CHAIR

Dheeraj Sanghi, Indian Institute of Technology, Kanpur  
Internet: dheeraj@iitk.ac.in

FINANCE CO-CHAIRS

A.K.P. Nambiar, Software Technology Park, Bangalore

Ajay Gupta, Western Michigan University

PUBLICITY CHAIR

Sandhya Dwarkadas, University of Rochester  
Internet: sandhya@cs.rochester.edu

LOCAL ARRANGEMENTS CHAIR

Kamal Karlapalem, Indian Institute of Information Technology, Hyderabad

PUBLICATIONS CHAIR

Nalini Venkatasubramanian, University of California, Irvine

STEERING COMMITTEE

Jose Duato, Universidad Politecnica de Valencia, Spain  
Viktor K. Prasanna, University of Southern California, Chair  
N. Radhakrishnan, US Army Research Lab  
Sartaj Sahni, University of Florida  
Assaf Schuster, Technion, Israel Institute of Technology, Israel

NATIONAL ADVISORY COMMITTEE

Alok Aggarwal, IBM Solutions Research Centre, New Delhi  
R.K. Bagga, DRDL, Hyderabad  
N. Balakrishnan, SERC, Indian Institute of Science  
Ashok Desai, Silicon Graphics Systems (India) Private Limited  
Kiran Deshpande, Mahindra British Telecom Limited  
H.K. Kaura, Bhabha Atomic Research Centre  
Hans H. Krafka, Siemens Communication Software Limited  
Ashish Mahadwar, PlanetAsia Limited  
Susanta Misra, Motorola India Electronics Limited  
Som Mittal, Digital Equipment (India) Limited  
B.V. Naidu, Software Technology Park, Bangalore  
N.R. Narayana Murthy, Infosys Technologies Limited  
S.V. Raghavan, Indian Institute of Technology, Chennai  
V. Rajaraman, Jawaharlal Nehru Centre for Advanced Scientific Research  
S. Ramadorai, Tata Consultancy Services, Mumbai  
K. Ramani, Future Software Pvt. Limited  
S. Ramani, National Centre for Software Technology  
Karthik Ramarao, Hewlett-Packard (India) Pvt. Limited  
Kalyan Rao, Satyam Computers Limited  
S.B. Rao, Indian Statistical Institute  
Uday Shukla, IBM (India) Limited  
U.N. Sinha, National Aerospace Laboratories